

FileViewEditToolsWindowHelp

Drafts

BRS:

BRS:

BRS:

BRS: knell

BRS: 19 21 23

BRS: 32with 33

Pending

Active

L1: (175943) (barrier near (height tunnel)) tunnel\$4 (energy diagram conduct\$4)

L2: (39120) (barrier near (height tunnel\$4)) (energy diagram conduct\$4) near bar

L3: (6062820) different second

L4: (1448) 3 adj 2

L5: (43918) float\$4 adj (gate electrode plate trap\$4) FG

L6: (167632) (control adj (gate electrode plate) CG word adj line wordline "WL")

L7: (8) 4 near 4 5

L8: (11) 4 near 4 6

L9: (14) 7 8

L10: (5) 7 and 8

L11: (8) 8 not 10

L12: (4) ("6461931" "6586797") pn.

Failed

Saved

Favorites

Tagged (5)

Browse

Queue

Clear

Dbs

USPAT,US,PCPUB,EPO,JPO,DERWENT,IBM,YDB

Plurals

Default operator

OR

Highlight all hit terms initially

("6461931" "6586797").pn.

April 2004

A BRS term

A BSR term

C Image

R Text

RS HTML

	U	I	Inventor	Document#	Issue	P	Title	Current	Current	XB	Retrieval	S	C	F	Image	Doc	P
1	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Forbes, Leon	US 6586797	20030	1	Graded composition gate insulators to reduce	257/325	257/314			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6586797	
2	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Eldridge, Jero	US 6461931	20021	1	Thin dielectric films for DRAM storage capa	438/388	257/E21.28			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6461931	
3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	ELDRIDGE, J	US 2003004	20030		Flash memory cell for reducing tunneling barr					<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>		
4	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	ELDRIDGE, J	US 6461931	20021		Formation of dielectric layer, used in capacit					<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>		

100

Default

RS HTML

Ready

MSJL